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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No 09/292,132
Filing Date April 14, 1999
Inventor Akram et al.
Assignee Micron Technology, Inc.
Group Art Unit
Examiner Mulpuri, Savitri
Attorney's Docket No MI22-1171
Title: Mathada of Formina A Transistor Cata

Title: Methods of Forming A Transistor Gate

RESPONSE TO MARCH 26, 2003 OFFICE ACTION

To:

Mail Stop Fee Amendment Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

From:

D. Brent Kenady

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Wells St. John P.S.

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<u>AMENDMENTS</u>

Responsive to the Office Action dated March 26, 2003, Applicant amends and remarks as follows:

<u>Underlines</u> indicate insertions and strikeouts indicate deletions.